



QP CODE: 26801806



Reg No :

Name :

IMCA DEGREE EXAMINATION APRIL 2026

Sixth Semester

Faculty of Technology and Applied Sciences

INTEGRATED MCA

Elective II - IMCA6E02C - DISTRIBUTED PROCESSING

2020 Admission Onwards

424E5C96

Time: 3 Hours

Maximum: 75 Marks

Part A

*Answer any **ten** questions*

*Each question carries **3** marks*

1. How does a distributed system differ from a centralized system?
2. Define the important styles of architecture for distributed systems.
3. What is system architecture?
4. Explain the advantage of using threads in non-distributed system.
5. What is middleware protocols?
6. Define the properties for Quality of Service(QoS).
7. Explain the difference between iterative and recursive name resolution.
8. What is vector clocks?
9. Define data-centric consistency in the context of distributed systems.
10. Define Message logging.
11. What is confidentiality and Integrity?
12. Define RSA algorithm.

(10×3=30 marks)

Part B

*Answer **all** questions*

*Each question carries **9** marks*

13. a) Discuss in detail about the transaction processing system.





OR

- b) Examine the important styles of architecture for distributed systems.
14. a) Explore the various models for code migration in distributed systems.

OR

- b) Discuss the different types of message oriented communication.
15. a) Explain the three types of naming system in detail.

OR

- b) Discuss in detail about Bully algorithm with a neat diagram.
16. a) Discuss the concept of fault tolerance in computer systems.

OR

- b) Discuss the concept of Atomic Multicast.
17. a) Explain the principles behind public key cryptography.

OR

- b) Provide an overview of authentication based on a shared secret key.

(5×9=45 marks)

