

MAHATMA GANDHI UNIVERSITY, KOTTAYAM
MGU-UGP (HONOURS)
FOURTH SEMESTER ELECTRONICS ELECTIVE PRACTICAL EXAMINATION
(2024 ADMISION ONWARDS)

MG4DSEECT205- Continuous and Discrete Systems

Duration: 2 Hours

Maximum Marks: 35

Practical Examination – Model Question Paper

Instructions:

Demonstrate the given task

Viva session (Minimum 7 questions from the Practical Module)

Lab Report

Evaluation Criteria:

Viva: 7 marks

Lab Report: 8 marks

Demonstration: 20 marks

1. Construct Op-amp – Square Wave Generator and study its output waveform.
2. Construct Op-amp – Digital/Analog Converter, observe and plot the values.
3. Construct Op-amp – Summing Amplifier and compare the theoretical values with the observed values.
4. Construct OP-Amp – inverter, non-inverter, buffer Amplifier for DC/AC signals.
5. Construct Op-amp Phase Shift Oscillator and plot the output frequency.
6. Construct Astable Multivibrartor using 555 and plot the output waveform.
7. Realization of logic gates – AND, OR and NOT – Using universal gates
8. Realization of Half Adder using universal gates.
9. Realization of Full adder using universal gates.
10. Verification of De Morgan's theorems – Using IC 7400
11. Realization of JK Flip Flops using IC 7400 & 7410 – Verification of truth table
12. Realization of 2 bit synchronous counter.