QP CODE: 24027340

24027340

Reg No : .....

Name : .....

B.Sc DEGREE (CBCS) REGULAR / IMPROVEMENT / REAPPEARANCE EXAMINATIONS, OCTOBER 2024

Third Semester

B.Sc Electronics Model III

Core Course - EL3CRT09 - 8085 MICROPROCESSOR

2017 Admission Onwards

F7BBB237

Time: 3 Hours Max. Marks : 80

Part A

*Answer any **ten** questions.*

*Each question carries **2** marks.*

1. Define RAM.
2. What are data transfer operations?
3. What are HOLD and HLDA signals?
4. Explain the function of auxiliary carry flag.
5. What is multiplexed address bus?
6. Explain XCHG instruction.
7. Explain the instruction RAL.
8. Explain the instruction JNC.
9. What is memory mapped I/O ?
10. Define interrupt.
11. Define maskable and non maskable interrupts.
12. What is the need for an ADC in Interfacing?

(10×2=20)

Part B

*Answer any **six** questions.*

*Each question carries **5** marks.*

13. Explain any external data operation of 8085.
14. Explain opcode fetch.
15. Explain why 8085 microprocessor is called an 8 bit microprocessor.
16. Write a program to add n numbers.
17. Explain the instructions related to subroutine.
18. Explain HLDA signal in 8085.
19. Explain the need for I/O ports in 8085.
20. Write a program to produce time delay using loop with in a loop .Draw the flow chart for the program also.
21. Explain mode 1 of 8255.

(6×5=30)

Part C

*Answer any **two** questions.*

*Each question carries **15** marks.*

22. With the help of timing diagrams explain the memory read and memory write operations.
23. What are the different addressing modes used in 8085? Give atleast 2 examples for each type and comment on their specialities.
24. With timing diagram explain the IN instruction.
25. Explain in detail the control word,control logic and modes of operation of 8255.

(2×15=30)