



QP CODE: 23104834

Reg No :

Name :

**B.Sc DEGREE (CBCS) REGULAR/IMPROVEMENT/REAPPEARANCE
EXAMINATIONS, FEBRUARY 2023**

First Semester

B.Sc Cyber Forensic Model III

Core Course - CF1CRT01 - COMPUTER ORGANIZATION

2019 Admission Onwards

26551A40

Time: 3 Hours

Max. Marks : 80

Part A

*Answer any **ten** questions.*

*Each question carries **2** marks.*

1. What is system software?
2. What are storage devices?
3. Define binary representation of decimal number 14.
4. Subtract 6 from 7 in binary form.
5. Write the logic expression for implementing overflow.
6. Write a note on multiplication of positive numbers.
7. List various arithmetic operations in ALU.
8. What you mean by branch instruction?
9. What is the use of hardwired control?
10. What is the use of interrupt?
11. Define cycle stealing.
12. What you mean by bus master?

(10×2=20)

Part B

*Answer any **six** questions.*

*Each question carries **5** marks.*





13. Explain different computer languages.
14. Explain input devices.
15. Explain different types of memory.
16. What is cache hit and cache miss?
17. Explain MAR and MDR.
18. Explain how integer division is performed in computer.
19. Explain MAR and MDR.
20. Explain uses of address decoder, data and status registers.
21. Explain sequence of events in handling an interrupt request.

(6×5=30)

Part C

*Answer any **two** questions.*

*Each question carries **15** marks.*

22. Explain in detail output devices.
23. Explain in detail addressing modes.
24. Define random access. Explain RAM.
25. Explain fetching a word from memory.

(2×15=30)

