



QP CODE: 24000691



24000691

Reg No :

Name :

B.Sc DEGREE (CBCS) REGULAR / REAPPEARANCE EXAMINATIONS, MARCH 2024

Sixth Semester

B.Sc Electronics and Computer Maintenance Model III

**CORE COURSE - EM6CRT06 - INTEL 8086 MICROPROCESSOR AND
PROGRAMMING**

2017 Admission Onwards

40D31A6A

Time: 3 Hours

Max. Marks : 80

Part A

*Answer any **ten** questions.*

*Each question carries **2** marks.*

1. What are functions of control unit?
2. Listout the classifications of microprocessors.
3. What is meant by embedded processors?
4. Define bus cycle.Give examples.
5. List two debug commands and mention their function.
6. What two results are produced by the hexadecimal command?
7. If SS=5000 and SP=FF00,Find the physical address of the top of the stack.
8. What is meant by linking?
9. Write a procedure to read a word from the keyboard?
10. What are the functions of INT 10H?
11. Differentiate real and virtual protected mode of 80286.
12. Compare 80386 and 80486.

(10×2=20)

Part B

*Answer any **six** questions.*

*Each question carries **5** marks.*

13. Differentiate vector and scalar processors.





14. Briefly explain the features of RISC processors.
15. Draw and explain the timing diagram for read bus cycle of 8086.
16. What is the use of memory models? What are the different types of memory models?
17. Explain different arithmetic instructions in 8086 processor.
18. Explain SCAS instruction.
19. What are the differences between procedure and macro?
20. Write a macro that will add two single digit numbers.
21. Write the features of 80186

(6×5=30)

Part C

*Answer any **two** questions.*

*Each question carries **15** marks.*

22. With neat diagram explain the pin diagram of 8086 and the functions of each pin.
23. Explain with example the various shift and rotate instructions in Intel 8086 instruction set.
24. Write an assembly language program to find the smallest of n number.
25. With neat diagram, explain the architecture of 80486, bringing out its salient features.

(2×15=30)

