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Reg No

Name

S) REGULAR / REAPPEARANCE / MERCY CHANCE

B.Sc DEGREE (CBCS) REGULAR / REAPPEARANCE / MERCY CHANCE EXAMINATIONS, FEBRUARY 2025

Sixth Semester

B.Sc Electronics and Computer Maintenance Model III

CORE COURSE - EM6CRT06 - INTEL 8086 MICROPROCESSOR AND PROGRAMMING

2017 Admission Onwards

3C90873D

Time: 3 Hours

Max. Marks : 80

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Part A

Answer any **ten** questions. Each question carries **2** marks.

- 1. Write the special functions carried by the general purpose register in 8086.
- 2. Define bus.Give its types.
- 3. What is the minimum mode of 8086? Write the components associated.
- 4. What is meant by hardware interrupts ?
- 5. What is the purpose of L command in DEBUG?
- 6. What is stack?Where it is located?
- 7. Write about SHL and SHR.
- 8. What are the files created after the assembly process?
- 9. Write a procedure to read a word from the keyboard.
- 10. Define INT 17H, Function 01H: Initialize printer port.
- 11. Write the features of 80186.
- 12. Write about the superscalar architecture of Pentium processor.

(10×2=20)

Part B

Answer any **six** questions. Each question carries **5** marks.

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- 13. Briefly explain the features of RISC processors.
- 14. Narrate the concept of (1) symbolic processor (2) embedded processor.
- 15. Draw and explain the timing diagram for read bus cycle of 8086.
- 16. What are the functions of assembler directives in a program execution?
- 17. Explain different arithmetic instructions in 8086 processor.
- 18. Explain CMPS instruction.
- 19. What are the advantages of macros?
- 20. Explain the different data movement steps that takes place when data is read from a Keyboard.
- 21. What are the features of 80286?

(6×5=30)

Part C

Answer any **two** questions.

Each question carries **15** marks.

- 22. In detail explain various types of micoprocessors. Give examples.
- 23. Explain how the various instructions of 8086 are classified.Give example in each type mentioning their significance.
- 24. Explain the table handling techniques.
- 25. With neat diagram, explain the architecture of 80486, bringingout its salient features.

(2×15=30)