P.D. Hills, 13.03.2020

## **NOTIFICATION**

In continuation to notification of even no. dated 10.03.2020, it is further notified that the time table for B.Sc. Electronics Model III (VI Semester CBCS 2017 Admission Regular Under Graduate Examinations March 2020) has been modified as under:

ELECTRONICS MODEL III		
Monday 16.03.2020	Core Course	Computer Networks
		Common to
		B.Sc Electronics and Computer Maintenance Model III
		B.Sc Electronics Model III
Wednesday 18.03.2020	Core Course	Optoelectronics
Tuesday 24.03.2020	Core Course	Digital Signal Processing
Thursday	Choice Based	Power Electronics
26.03.2020	Core Course I	
		Mobile Communication
		Digital Image Processing

VIGI T A JOINT REGISTRAR I (EXAM)

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- 1. The Principals of Colleges concerned
- 2. PS to VC/PVC
- 3. PRO for a Press Release/Enquiry
- 4. JR I/DR IV/ VIII/AR XI/XIV/XV/XVIII/XIX/XXI
- 5. PA to C.E/C.E's Sn/CT&D Sn/EN I, IV/CETEX/Front Office
- 6. EB/Tabulation Sns. concerned/Ac.C/Ac.AV/EK Sns
- 7. Exam. Store /Exam. Tappal /IQAC/Content Management Sns
- 8. SF/FC